* Brainstorm Testing
  + What is the objective
    - Have a Modem configured for receiving and transmitting BPSK
      * Modulation and Demodulation is done in Verilog
    - Communicate to a terminal node controller
  + How you plan on doing?
    - Program an FPGA to modulate and demodulate a BPSK signal using the given model
      * FPGA of choice it needs I think most FPGAs will have them
        + DAC
        + ADC
  + Testing
    - ISE Design
      * Timing
      * Test-bench
      * DAC/ ADC Make sure we can read the analog signals
    - Matlab
      * Use Matlab to plot all of our signals